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Bib Data Sheet

CONFIRMATION NO. 3957

SERIAL NUMBER 10/790,190	FILING DATE 03/02/2004 RULE	CLASS 257	GROUP ART UNIT 2818	ATTORNEY DOCKET NO. 520.43558X00
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** CONTINUING DATA *****

(None)

** FOREIGN APPLICATIONS *****

JAPAN P2003-172912 06/18/2003 TH

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/20/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Examiner's Signature <u>TH</u> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 16	TOTAL CLAIMS 17	INDEPENDENT CLAIMS 7
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TITLE

Semiconductor device, semiconductor circuit module and manufacturing method of the same

FILING FEE RECEIVED 1244	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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